mey Docket No.BGJ-101

Expedited Procedure under 37 C.F.R. § 1.116

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Joachim Schnabel, et al.	Confirmation No.: 1603
Serial No.:	10/663,354	Group Art Unit: 2832
Filed:	16 September 2003	Examiner: Karl D. Easthom
Title	Arrangement Of Several Resistors Jointly Positioned In A Well Of A Semiconductor Device, And A Semiconductor Device Including At Least One Such Arrangement	BOX AF WIRCE q.24.05

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 23, 2005.

> Attorney for Applicants Registration No 43,474

Dated: August 23, 2005

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE AFTER FINAL REJECTION UNDER 37C.F.R.§ 1.116

Sir:

This Amendment After Final Rejection is filed in lieu of a Notice of Appeal in response to the Final Office Action mailed from the U.S. Patent & Trademark Office on June 23, 2005 in the above-identified application. A Notice of Appeal will be due on September 23, 2005. This response is filed within 2 months of the mailing date of the Office Action, thereby enabling the Applicants to benefit from the extension of time policy as set forth in MPEP §706.07(f). Entry of the identified amendments and allowance of the claims is respectfully requested.

Amendments to the Claims are reflected in the listing that begins on page 2 herein. Remarks/Arguments begin on page 5 herein.